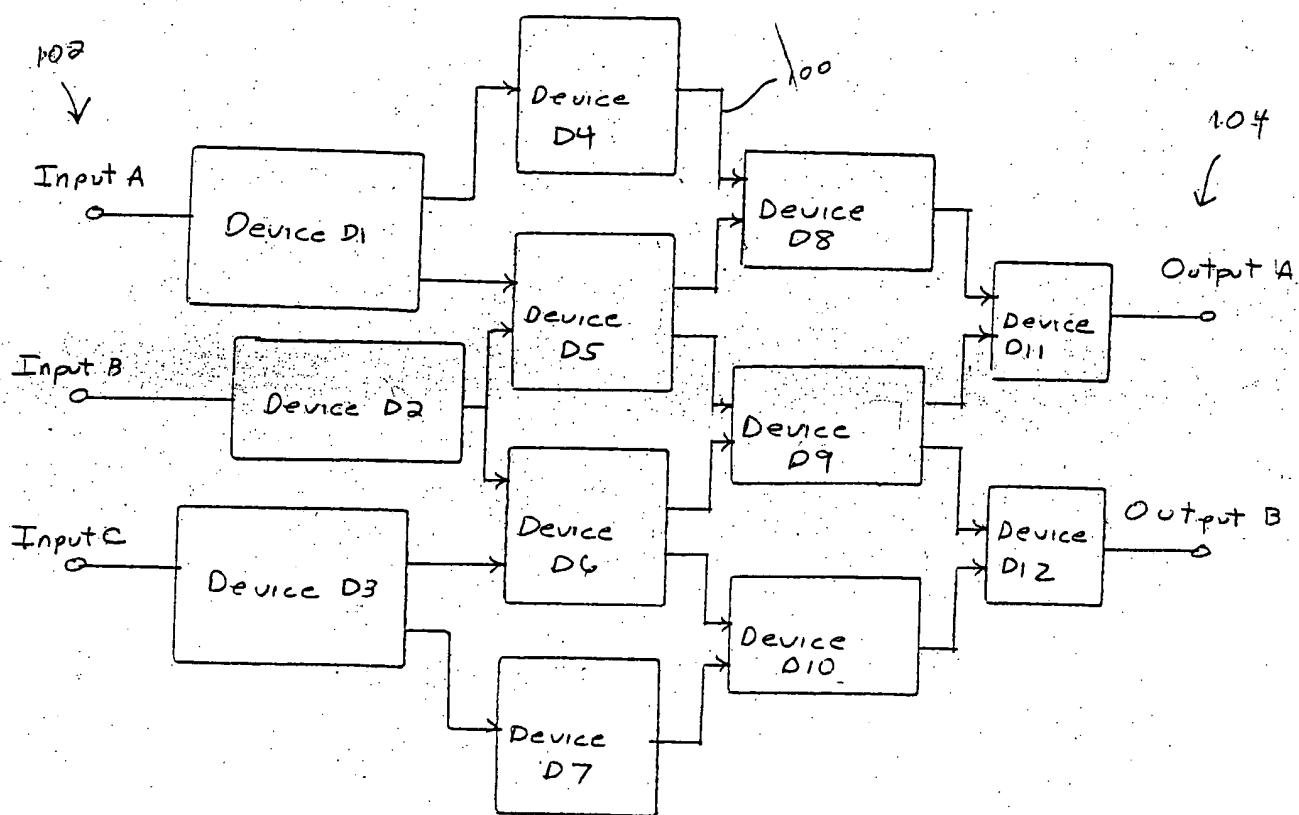


2  
6

Fig 2



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Initial  
Device  
Parameters

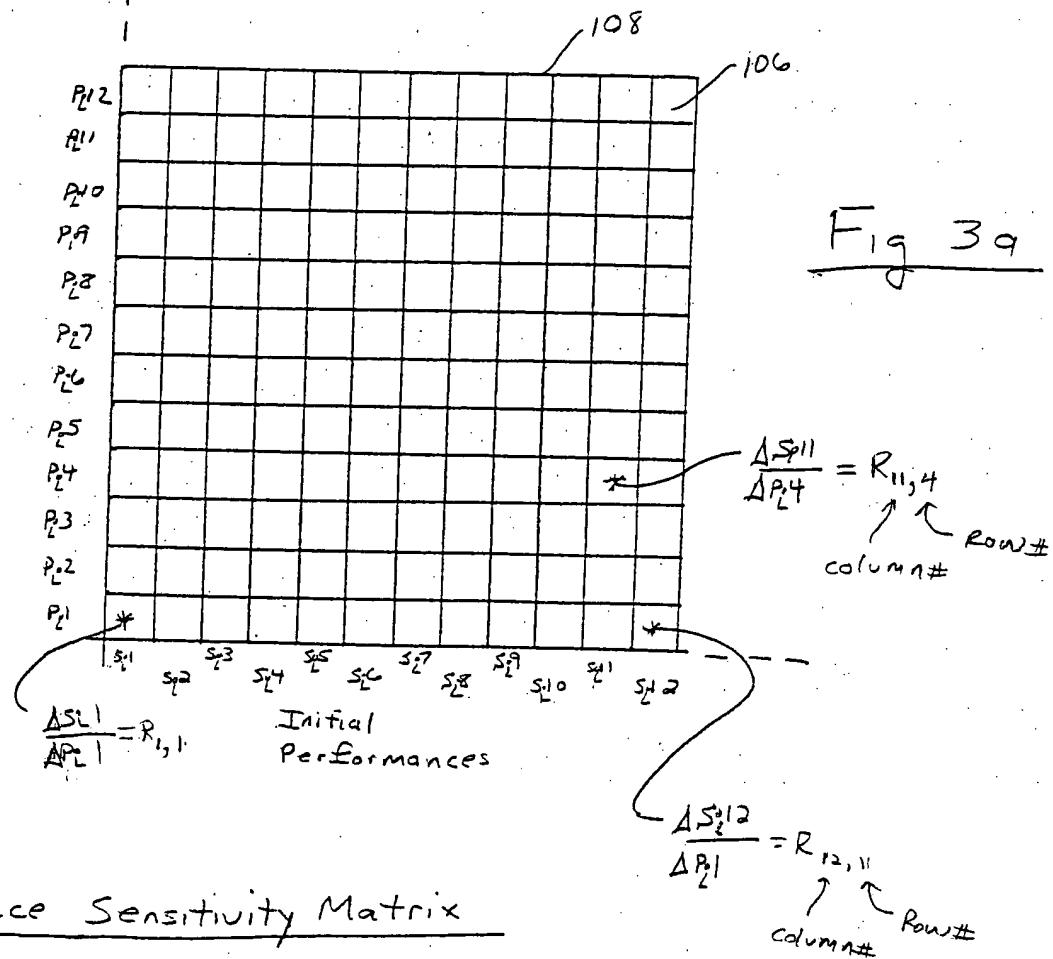
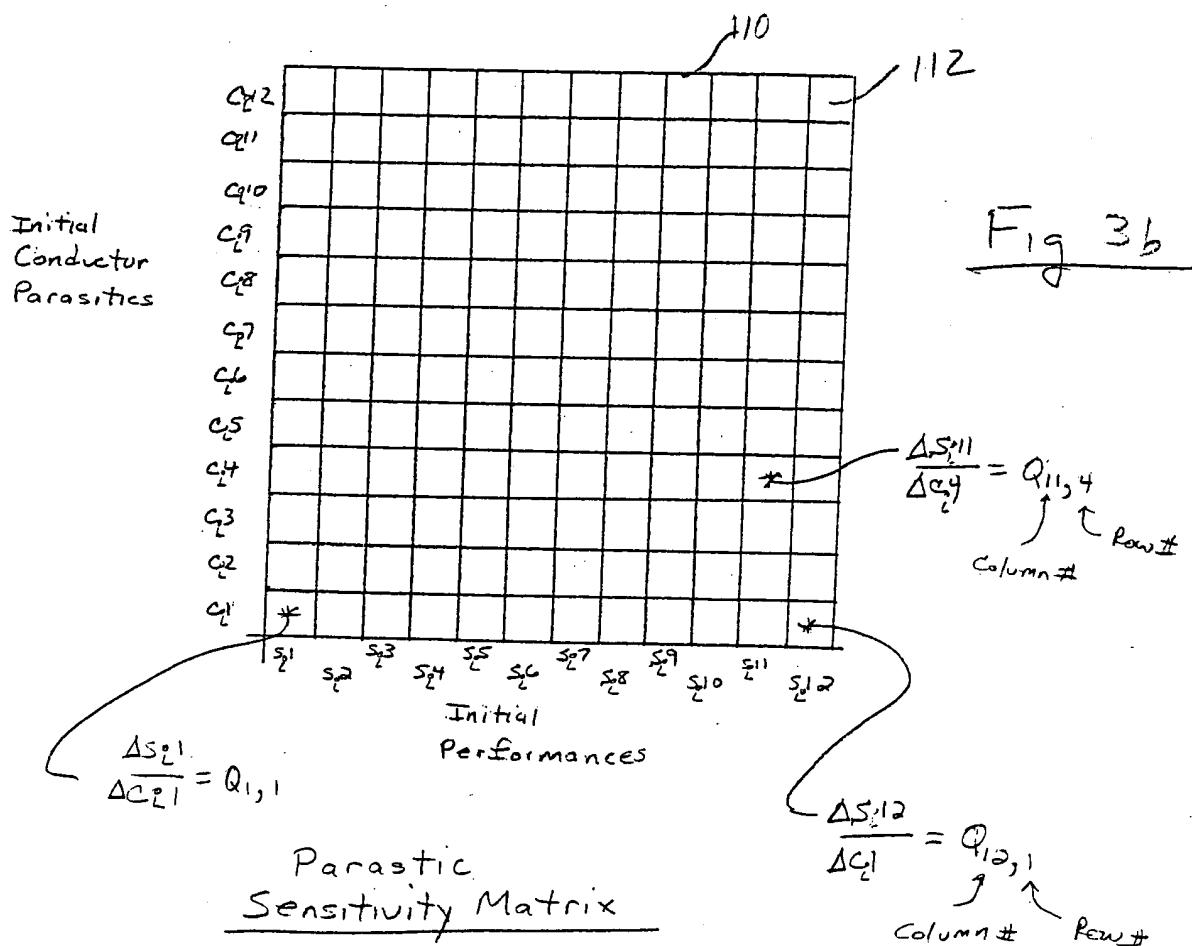


Fig 39

4  
6



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Fig 4

Target Performance Specification	Tolerance
T 1	$\pm x_1\%$
T 2	$+ x_2\%$
T 3	$+ x_3$
T 4	$- x_4$
T 5	$\pm x_5$
T 6	$- x_6\%$
1	1
1	1
1	1

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Fig 5

